



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|             |   |            |                    |
|-------------|---|------------|--------------------|
| Applicant : | Wolrich, et al.   | Art Unit : | 2187               |
| Serial No.: | 10/780,330  | Examiner : | Prasith Thammavong |
| Filed :     | February 17, 2004   | Assignee : | Intel Corporation  |
| Title :     | MEMORY MAPPING IN A PROCESSOR HAVING MULTIPLE<br>PROGRAMMABLE UNITS |            |                    |

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION MAILED 5/12/2009

**Remarks**

...page 2

OK TO ENTER: /PT/

06/14/2009